

Agenda
STAR iTPC DOE Technical, Cost, Schedule, and Management Review

Day 1, September 13, 2016

8:30-9:00 Executive Session

9:00 – 9:20 Physics and merits of iTPC Z.Xu 15+5

9:20 – 10:00 Overview 25+15 F.Videbaek

- What is iTPC (overview for new reviewers)
- KPP's and UPP's
- Components and fabrication path
- Brief schedule and cost
- Management Structure

10:00 – 10:45 Mechanical – Strongback and padplane joining J.Thomas 30+15

- Design
- Fabrication – QA
- Padplane design and fabrication
- Padplane joining work
- Cost, schedule (SOW)
- Risks and mitigation

Break 10:45- 11:00

11:00-12:00 Multi Wire Chamber production Q.Xu (Remote SDU) 40+20

- Prototyping at SDU
- Production Plans
- Testing and QA
- Risks and Mitigation
- China resources

12:00-1:15 Working Lunch/Executive Session

1:15-1:45 Installation and Insertion Tooling R.Sharma 20+10

- Insertion tooling design and status
- Installation plans and schedule
- Risks and mitigation

1:45-2:45 Electronics T.Ljubijic 40+20

- Design Overview
- iFEE
- iRDO
- SAMPA chip development
- Risks and mitigation

2:45-3:00 Break

3:00-4:30 Cost & Schedule, Management F.Videbaek 60+30

- Cost
- Schedule (float, critical path)
- milestones
- Response to directors review
- Risk evaluation
- KPPs and UPPs
- Management structure, reporting
- Commissioning and detector readiness

4:30-7:30 Executive Session

Day 2, September 14, 2016

8:30-9:30 Homework

9:30-11:30 Breakout Sessions

11:30-12:00 Executive Session

12:00-1:00 Working Lunch/Executive Session

1:00-4:00 Executive Session